

FIG. 1

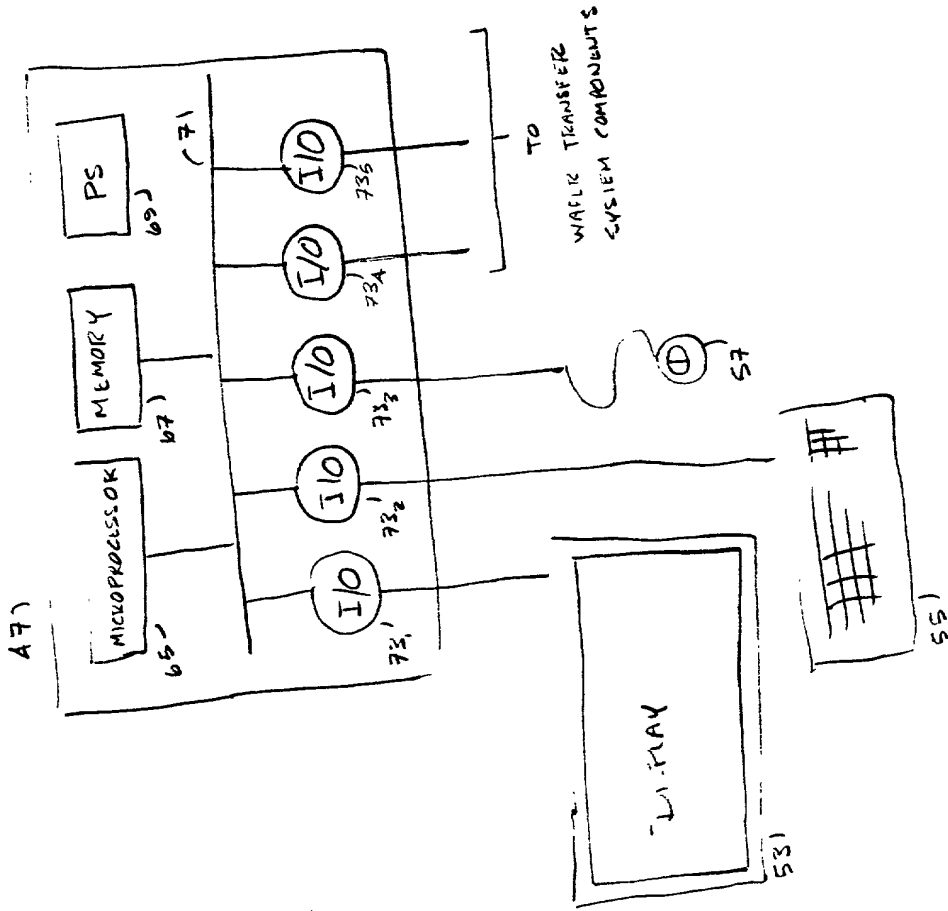


FIG. 2

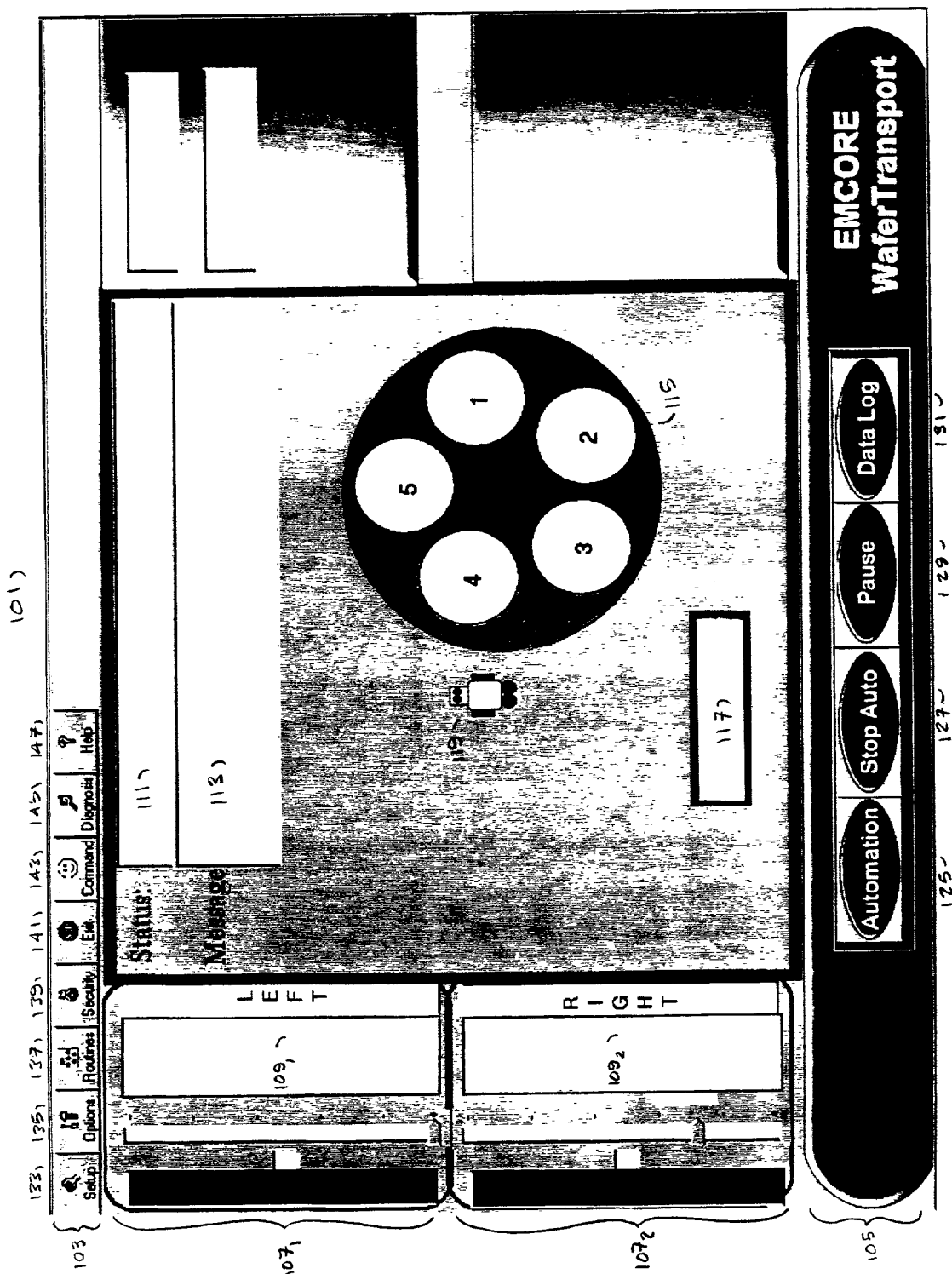


Fig. 3

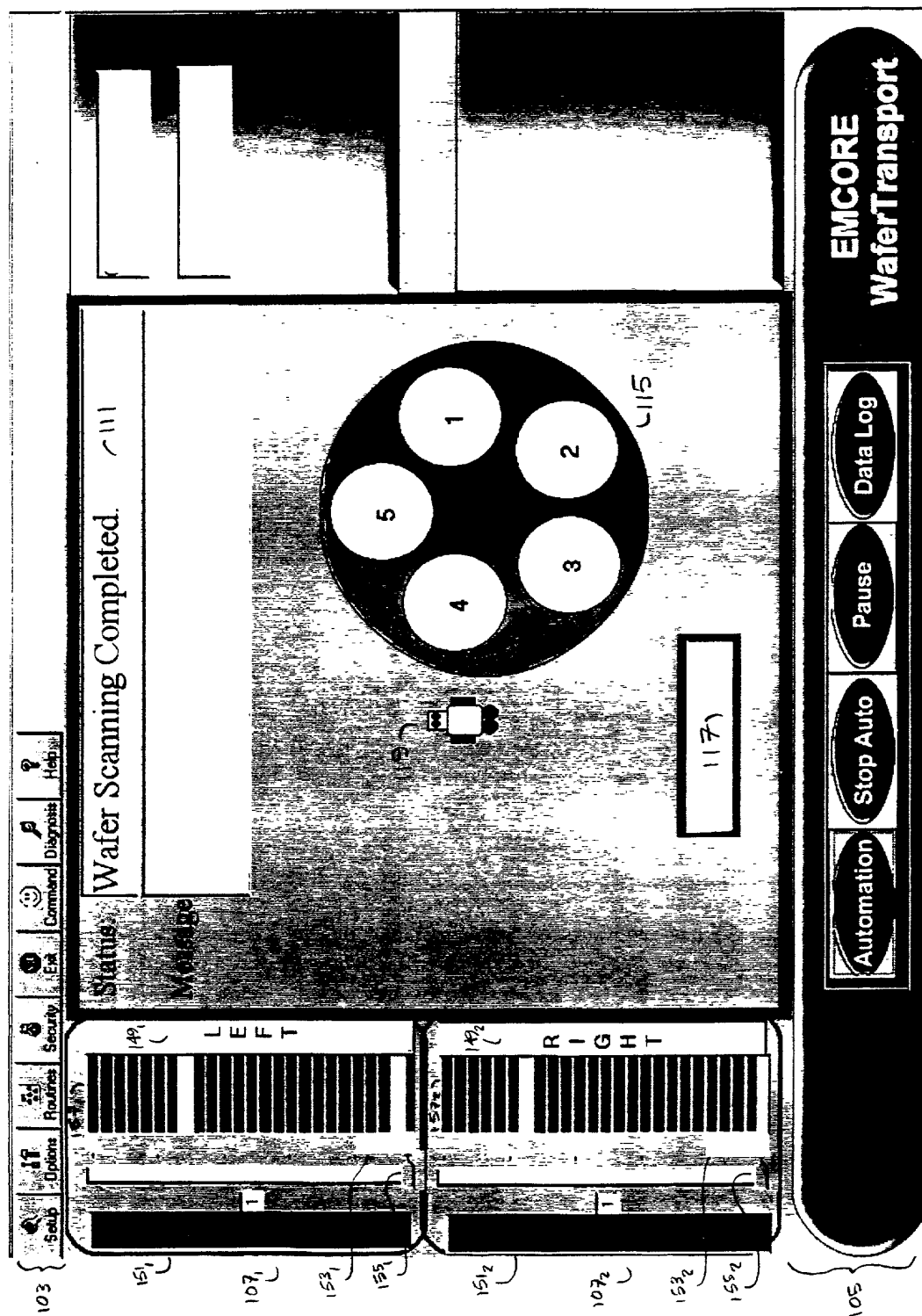


FIG. 4

EMCORE Wafer Transport

101

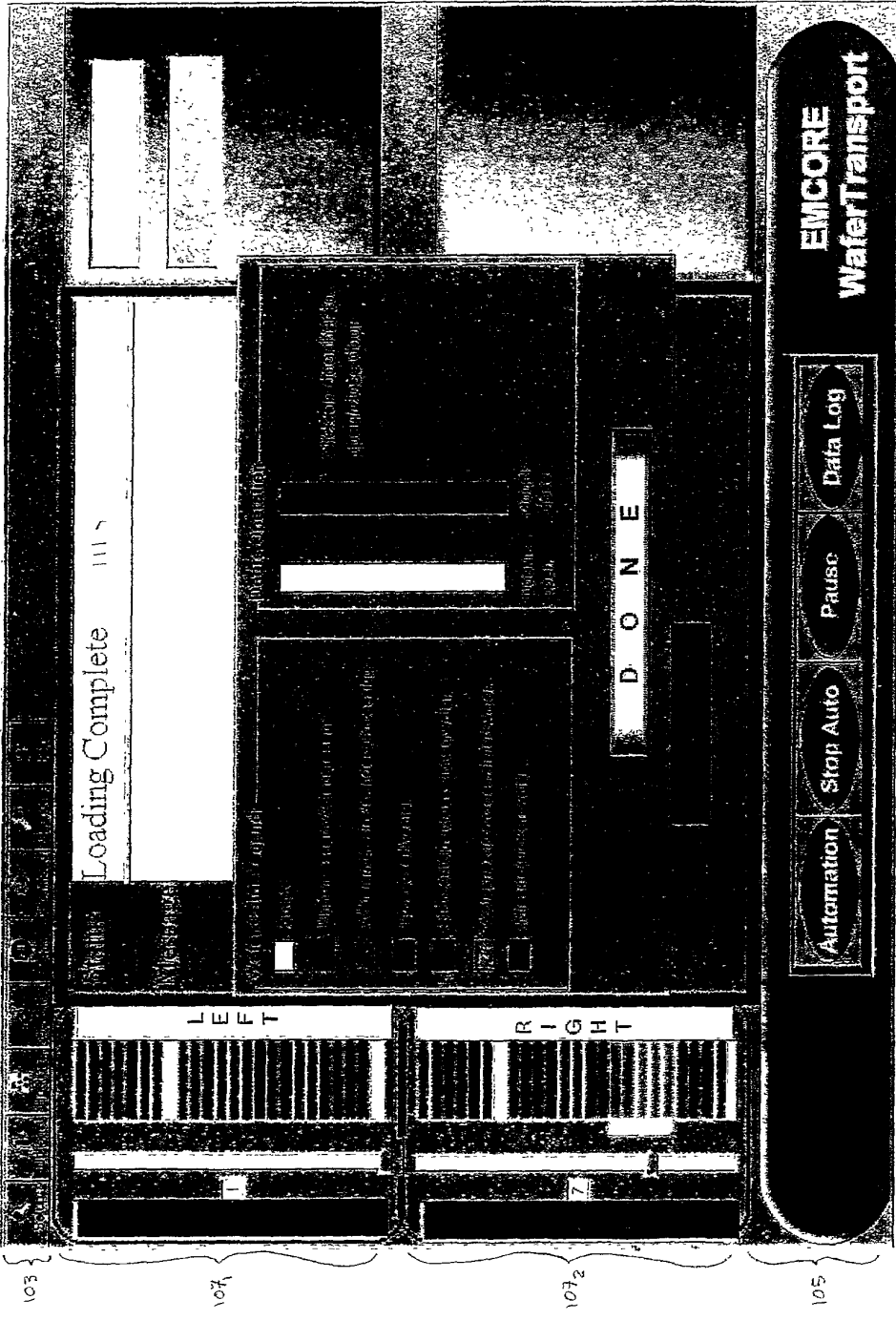


FIG. 5

101 >

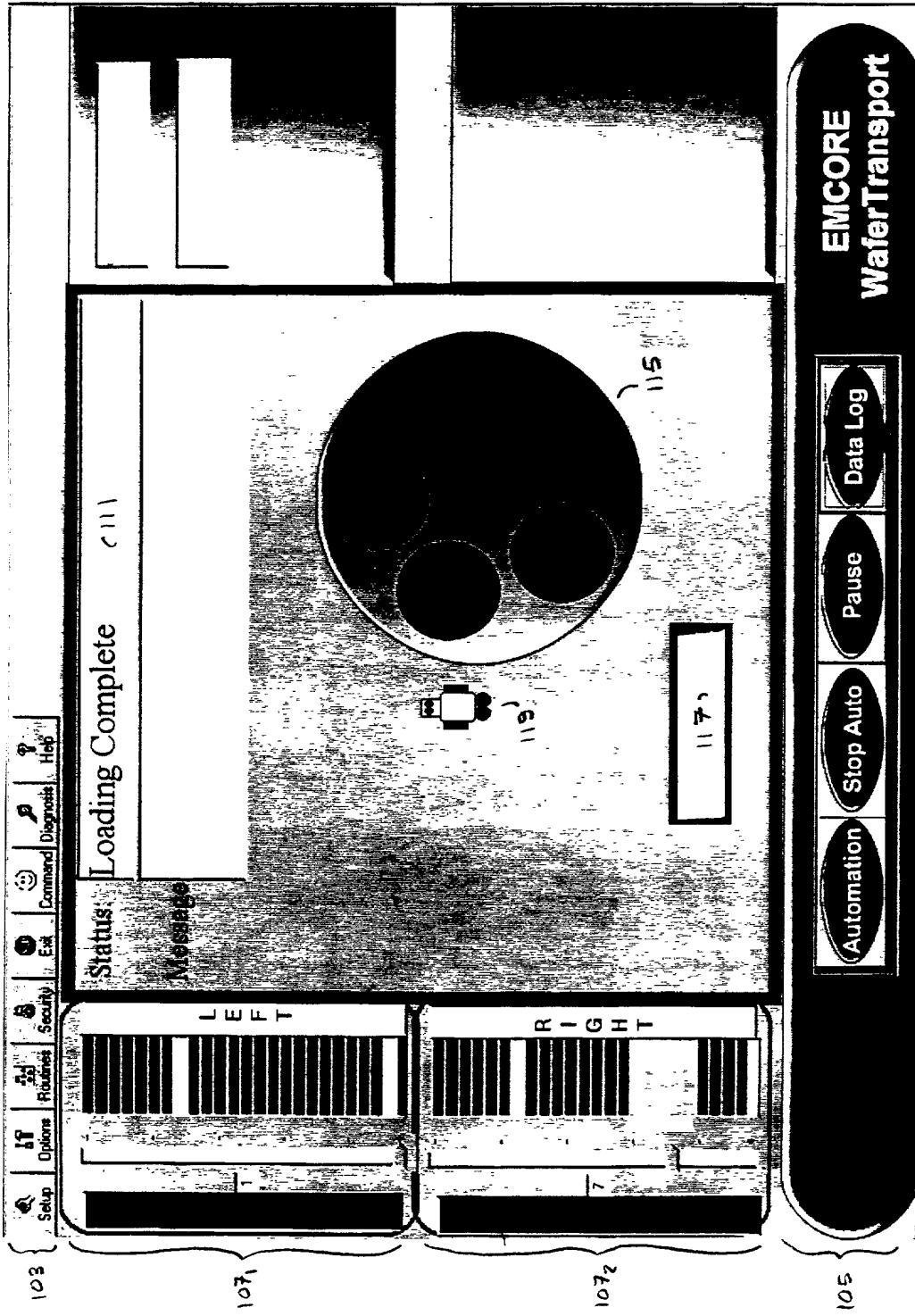


FIG. 6

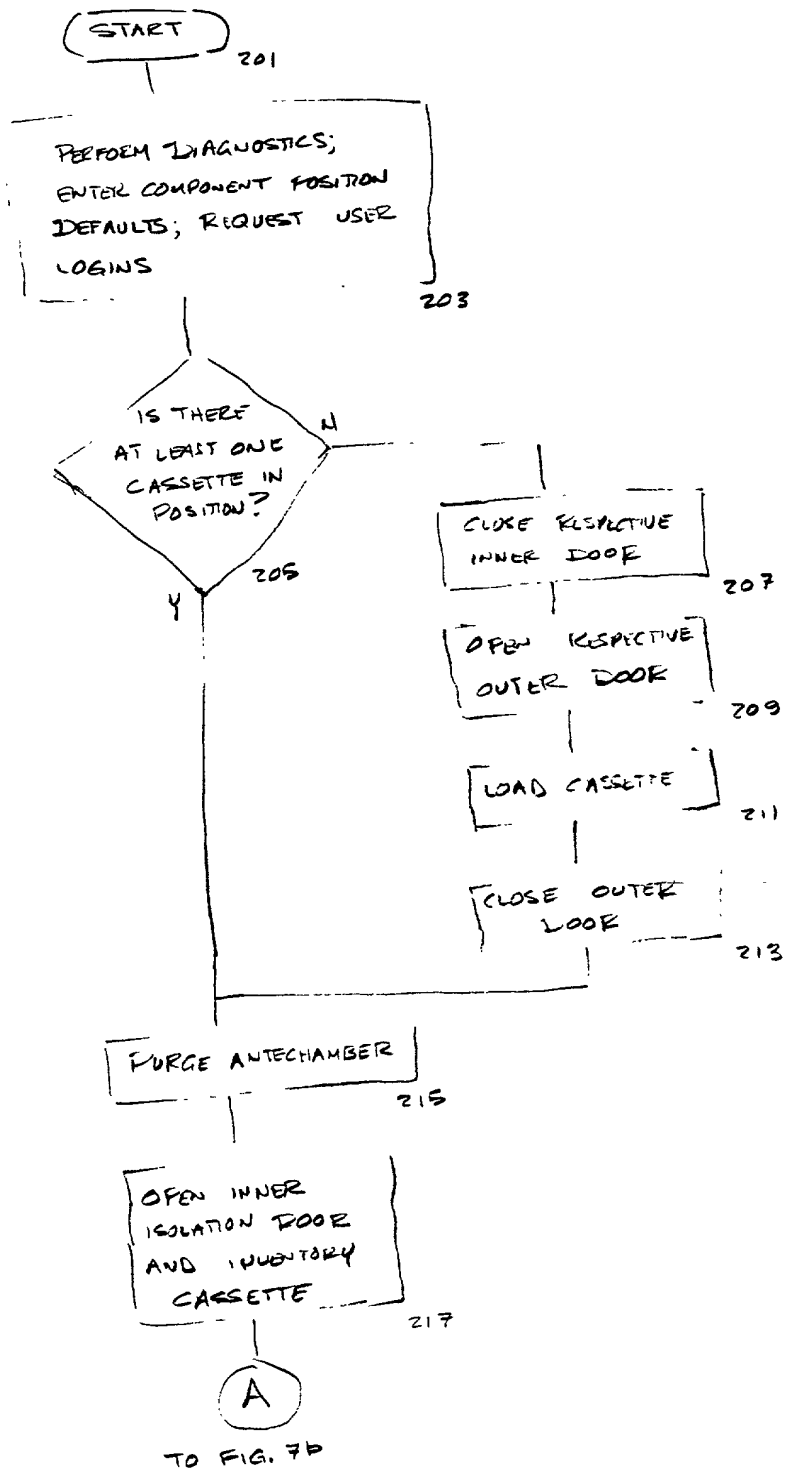


FIG. 7a

FROM FIG. 7a

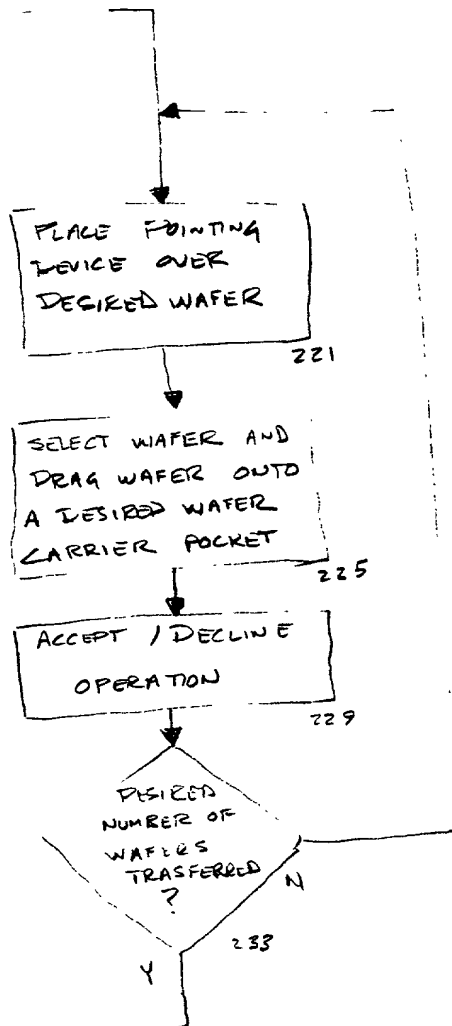
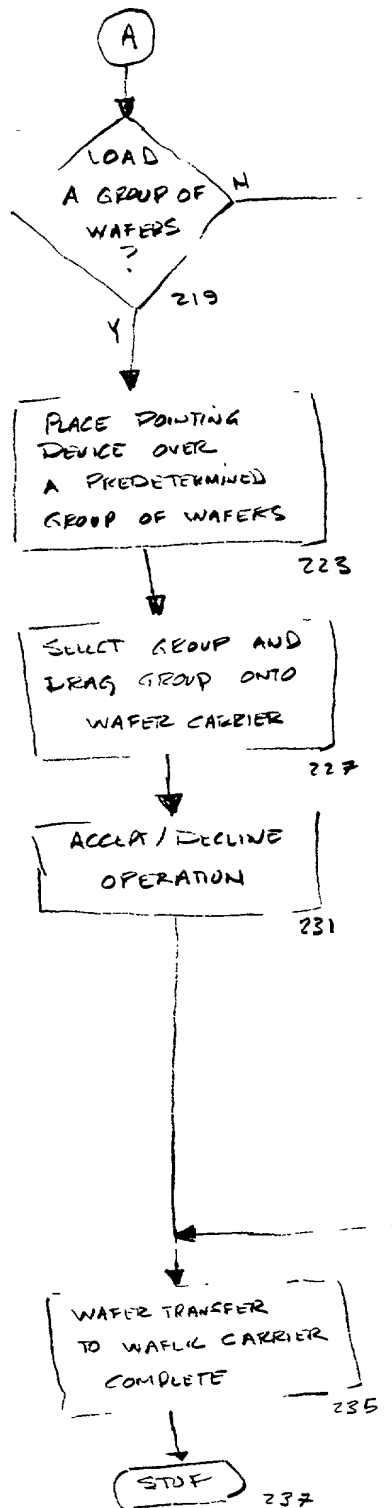
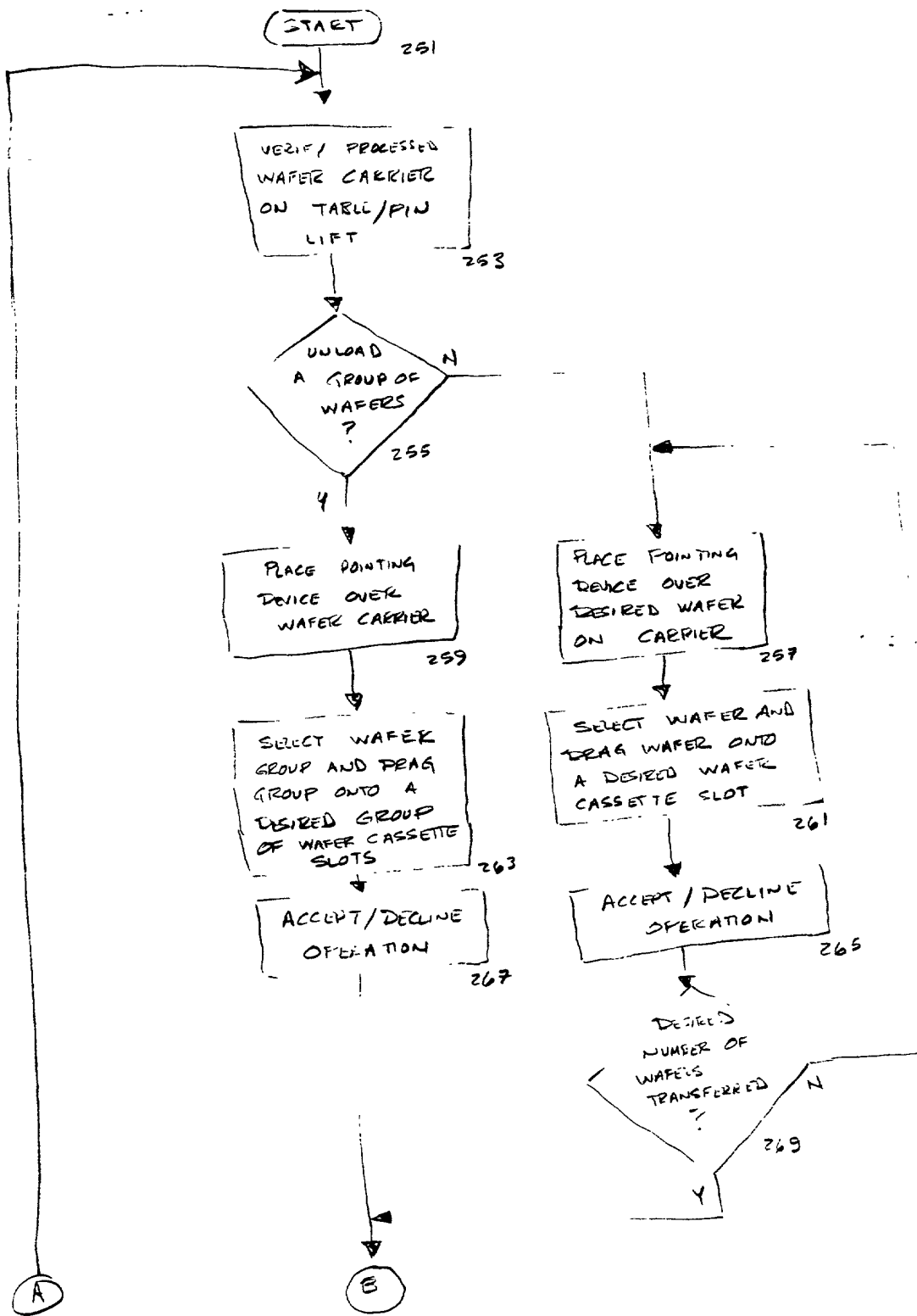


FIG. 7b





FROM FIG. 8b

TO FIG. 8b

FIG. 8a

TO FIG. 8a

FROM FIG. 8a

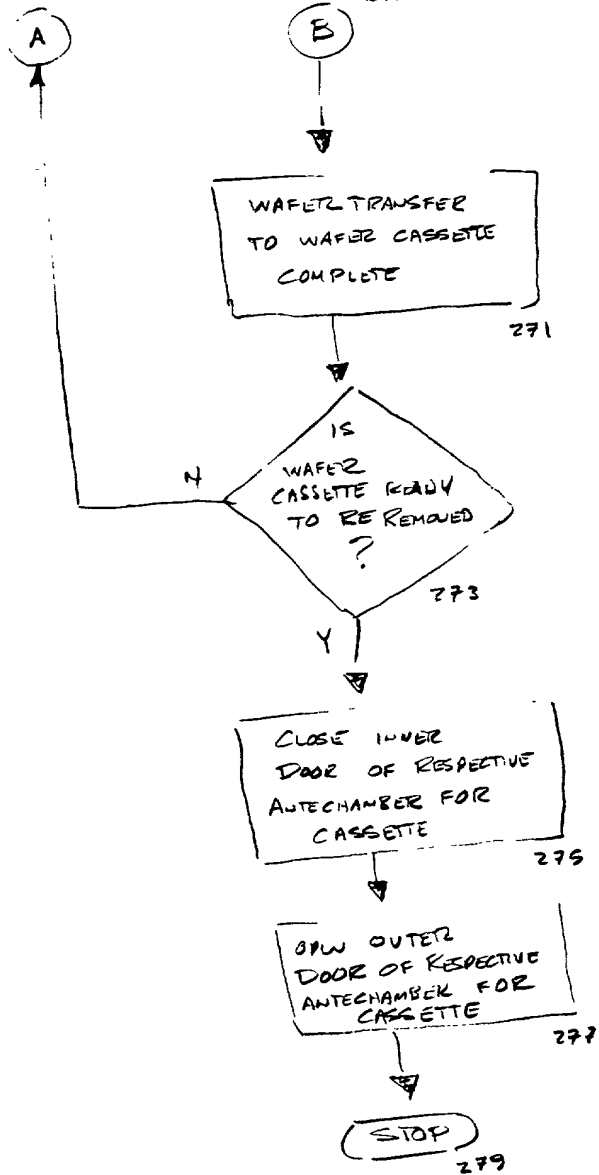


FIG. 8b